

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"5568345".pn.	USPAT; EPO; JPO	OR	ON	2006/06/19 13:31
L2	1	"5568345".pn. and divid\$5	USPAT; EPO; JPO	OR	ON	2006/06/19 13:32
L3	8	("5568345").URPN.	USPAT	OR	ON	2006/06/19 13:32
L4	4	("4282556" "4704654" "4821136" "5050060"). PN.	US-PGPUB ; USPAT; USOCR	OR	ON	2006/06/19 13:34
L5	32	(US-20040150930-\$ or US-20030011031-\$).did. or (US-6919602-\$ or US-6671149-\$ or US-6560081-\$ or US-6548869-\$ or US-6504418-\$ or US-6347026-\$ or US-6201680-\$ or US-6144539-\$ or US-5978192-\$ or US-5903184-\$ or US-5731729-\$ or US-5565790-\$ or US-5555152-\$ or US-5538907-\$ or US-5523714-\$ or US-5508548-\$ or US-5276582-\$ or US-5091818-\$ or US-4975798-\$ or US-4937700-\$ or US-4385337-\$ or US-4347475-\$ or US-4303841-\$ or US-4288694-\$ or US-4020395-\$ or US-3991381-\$).did. or (US-3582713-\$).did. or (EP-323643-\$).did. or (JP-2004229365-\$ or JP-03134514-\$).did.	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2006/06/19 13:37
L6	9	5 and capacitor with resistor	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:49
L7	30	gate with capacitor with resistor same esd same clamp	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:50

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L8	1	gate with first adj resistor with second adj resistor same esd same clamp	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:54
L9	251	esd with detection	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:54
L10	13	esd with detection and first adj resistor	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:55
L11	7	esd with detection same first adj resistor	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/19 13:55
S1	2	"20040150930"	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 09:28
S3	28	input near resistor same voltage adj limiter	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/13 13:02
S4	176	input near5 resistor same voltage adj limiter	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:13
S5	435	input near5 resistor same voltage adj (limiter limiting)	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:13

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S6	49	input near5 resistor same voltage adj (limiter limiting) and "361"/\$.ccls.	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:17
S7	30	input near3 voltage with resistor same voltage adj (limiter limiting) and "361"/\$.ccls.	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:34
S8	9	input near3 voltage with resistor same voltage adj (limiter limiting) same zener and "361"/\$.ccls.	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:52
S9	0	input near3 voltage same voltage adj (limiter limiting) same zener same ic and "361"/\$.ccls.	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:53
S10	11	input near3 voltage same voltage adj (limiter limiting) same zener same (ic integrated adj circuit)	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:55
S11	103	input near3 voltage same voltage adj (limiter limiting) same (ic integrated adj circuit)	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/14 16:55
S12	22	("4937700").URPN.	USPAT	OR	ON	2006/06/13 13:01
S13	106	input near resistor same voltage adj limit\$3	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/13 13:02

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S14	4	input near resistor same voltage adj limit\$3 same IC	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/13 13:03
S15	37	input near5 resistor same voltage adj limit\$3 same IC	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/13 13:22
S16	796	361/91.1,91.2.ccls.	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/13 13:23
S17	568	361/91.1,91.2.ccls.	USPAT	OR	ON	2006/06/13 13:24
S18	169	S17 and voltage near3 limit\$3	USPAT	OR	ON	2006/06/13 13:59
S19	66	S17 and voltage near3 limit\$3 same resistor	USPAT	OR	ON	2006/06/13 14:00
S20	18	S17 and voltage near3 limit\$3 same resistor same input with voltage	USPAT	OR	ON	2006/06/13 14:01
S21	18	S17 and voltage near3 limit\$3 same resistor same input with voltage	US-PGPUB ; USPAT; EPO; JPO	OR	ON	2006/06/13 14:01
S22	18	S17 and voltage near3 limit\$3 same resistor same input with voltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:03
S23	4653	voltage near3 limit\$3 same resistor same input with voltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:03
S24	2234	voltage near3 limit\$3 with resistor same input with voltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:03
S25	1437	voltage near3 limit\$3 with resistor with input with voltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:03

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S26	414	voltage near3 limit\$3 with resistor with input near voltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:04
S27	13	voltage near3 limit\$3 with resistor with input near voltage same overvoltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 14:18
S28	27	zener with resistor with input near voltage same overvoltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 15:26
S29	8	zener same current adj limit\$3 same input near voltage same overvoltage	US-PGPUB ; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/13 15:27
S30	21905	zener near5 resistor	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 09:28
S31	527	zener near5 resistor same overvoltage	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 09:29
S32	431	zener near4 resistor same overvoltage	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 09:29
S33	33	zener near4 resistor same overvoltage same (ic integrated adj circuit)	US-PGPUB ; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/15 09:30
S34	18	zener near4 resistor same overvoltage same (ic integrated adj circuit)	USPAT	OR	ON	2006/06/15 09:32
S35	7	zener near4 resistor same overvoltage same (ic integrated adj circuit) and "361"/\$.ccls.	USPAT	OR	ON	2006/06/15 11:21

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S36	1	"4303841".pn.	USPAT	OR	ON	2006/06/15 11:57
S37	12	("4303841").URPN.	USPAT	OR	ON	2006/06/15 11:51
S38	648	isolating adj resistor	USPAT	OR	ON	2006/06/15 11:51
S39	0	isolating adj resistor same printed adj board	USPAT	OR	ON	2006/06/15 11:52
S40	5887	printed adj board	USPAT	OR	ON	2006/06/15 11:52
S41	2	S40 and S38	USPAT	OR	ON	2006/06/15 11:52
S42	891	voltage adj limit\$5 same current adj limit\$3	USPAT	OR	ON	2006/06/15 11:58
S43	226	voltage adj limit\$5 same current adj limit\$3 near5 resistor	USPAT	OR	ON	2006/06/15 11:58
S44	104	voltage adj limit\$5 with current adj limit\$3 near5 resistor	USPAT	OR	ON	2006/06/15 12:04
S45	5	voltage adj limit\$5 with current adj limit\$3 near5 resistor same (transient overvoltage)	USPAT	OR	ON	2006/06/15 12:31
S46	5523	ic and (voltage adj limit\$3 zener)	USPAT	OR	ON	2006/06/15 12:32
S47	259	ic and (voltage adj limit\$3 zener) same overvoltage	USPAT	OR	ON	2006/06/15 12:32
S48	47	ic same (voltage adj limit\$3 zener) same overvoltage	USPAT	OR	ON	2006/06/15 12:32
S49	25	ic same (voltage adj limit\$3 zener) same overvoltage same resistor	USPAT	OR	ON	2006/06/15 12:32
S50	2	("4334256" "5027250"). PN.	US-PGPUB ; USPAT; USOCR	OR	ON	2006/06/15 14:32
S51	4	("5091818").URPN.	USPAT	OR	ON	2006/06/15 17:02
S52	0	voltage adj limiter same bpj same transistor same diode	USPAT	OR	ON	2006/06/15 17:03
S53	0	voltage adj limiter same bpj same diode	USPAT	OR	ON	2006/06/15 17:03
S54	206	voltage adj limiter same transistor same diode	USPAT	OR	ON	2006/06/15 17:04
S55	6	voltage adj limiter same transistor same diode same overvoltage	USPAT	OR	ON	2006/06/15 17:05

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S56	44	voltage adj limiter same transistor same diode same ground	USPAT	OR	ON	2006/06/15 17:07
S57	2	voltage adj limiter same transistor same diode same ground same bipolar	USPAT	OR	ON	2006/06/15 17:09
S58	1	voltage adj limiter same base same diode same ground same bipolar	USPAT	OR	ON	2006/06/15 17:10
S59	1027	(transient overvoltage surge) same base same diode same ground same transistor	USPAT	OR	ON	2006/06/15 17:11
S60	375	(overvoltage surge) same base same diode same ground same transistor	USPAT	OR	ON	2006/06/15 17:11
S61	132	(overvoltage) same base same diode same ground same transistor	USPAT	OR	ON	2006/06/18 14:45
S62	996	(overvoltage clamp) same base same (diode zener) same ground same transistor	USPAT	OR	ON	2006/06/18 16:40
S63	54	(overvoltage clamp) same base same (diode zener) same ground same transistor with bipolar	USPAT	OR	ON	2006/06/18 14:46
S64	11	("20020153533" "3612915" "4396999" "4496854" "5239440" "5276350" "5276582" "5538907" "6465848" "6560081" "6670792"). PN.	US-PGPUB ; USPAT; USOCR	OR	ON	2006/06/18 14:47
S65	555	(overvoltage clamp) with (zener) same ground	USPAT	OR	ON	2006/06/18 15:48
S66	217	(overvoltage clamp) with (zener) with ground	USPAT	OR	ON	2006/06/18 15:48
S67	231	(overvoltage clamp voltage adj limiter) with (zener) with ground	USPAT	OR	ON	2006/06/18 15:48
S68	95	(overvoltage voltage adj limiter) with (zener) with ground	USPAT	OR	ON	2006/06/18 15:49
S69	14	(voltage adj limiter) with (zener) with ground	USPAT	OR	ON	2006/06/18 15:50
S70	64	(voltage adj limiter) with (zener) with resistor	USPAT	OR	ON	2006/06/18 16:14
S71	233	(overvoltage) with (zener) with resistor	USPAT	OR	ON	2006/06/18 16:14
S72	953	(overvoltage) with (zener)	USPAT	OR	ON	2006/06/18 16:14

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S73	244	(voltage adj limiter) with (zener)	USPAT	OR	ON	2006/06/18 16:23
S74	30	(voltage adj limiter) with (zener) same DC	USPAT	OR	ON	2006/06/18 16:25
S75	5	(overvoltage) with (zener) same DC same ground with input	USPAT	OR	ON	2006/06/18 16:26
S76	30	(overvoltage) with (zener) same ground with input	USPAT	OR	ON	2006/06/18 16:27
S77	37	(overvoltage) with (zener) same ground same input same between	USPAT	OR	ON	2006/06/18 16:31
S78	66	(overvoltage) same (zener) same ground same input same between	USPAT	OR	ON	2006/06/18 16:33
S79	89	(surge) same (zener) same ground same input same between	USPAT	OR	ON	2006/06/18 16:35
S80	10	(surge) same (zener) same ground same input same between same DC	USPAT	OR	ON	2006/06/18 16:35
S81	2	("6229180" "6441463"). PN.	US-PGPUB ; USPAT; USOCR	OR	ON	2006/06/18 16:39
S82	1809	(overvoltage clamp) same (diode zener) same ground same between	USPAT; EPO; JPO	OR	ON	2006/06/18 16:41
S83	331	(clamp) same (zener) same ground same between	USPAT; EPO; JPO	OR	ON	2006/06/18 16:41
S84	124	(clamp) same (zener) with ground with between	USPAT; EPO; JPO	OR	ON	2006/06/18 16:42
S85	11	(clamp) same dc same (zener) with ground with between	USPAT; EPO; JPO	OR	ON	2006/06/18 17:40
S86	0	(clamp) same dc same MOS same gate same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:43
S87	6	(clamp) same dc same MOS same gate same resistor	USPAT; EPO; JPO	OR	ON	2006/06/18 17:40
S88	0	(clamp) same dc same MOS same gate same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:44
S89	0	(overvoltage) same dc same MOS same gate same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:44
S90	0	(overvoltage) same dc same MOS same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:44
S91	5	(overvoltage) same dc same MOS same resistor	USPAT; EPO; JPO	OR	ON	2006/06/18 17:46
S92	0	(voltage near3 limit5) same dc same MOS same resistor	USPAT; EPO; JPO	OR	ON	2006/06/18 17:46

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S93	0	(voltage near3 limit5) same MOS same resistor	USPAT; EPO; JPO	OR	ON	2006/06/18 17:47
S94	266	(voltage near3 limit\$5) same MOS same resistor	USPAT; EPO; JPO	OR	ON	2006/06/18 17:47
S95	37	(voltage near3 limit\$5) same MOS same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:52
S96	2	(voltage near3 limit\$5) same MOS same resistor near first	USPAT; EPO; JPO	OR	ON	2006/06/18 17:54
S97	3	(overvoltage) same MOS same resistor near first	USPAT; EPO; JPO	OR	ON	2006/06/18 17:55
S98	24245	second near resistor MOS same resistor near first	USPAT; EPO; JPO	OR	ON	2006/06/18 17:55
S99	365	second near resistor same MOS same resistor near first	USPAT; EPO; JPO	OR	ON	2006/06/18 17:56
S10 0	0	MOS same gate same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:57
S10 1	0	MOS same gate same divider\$3	USPAT; EPO; JPO	OR	ON	2006/06/18 17:57
S10 2	0	gate same divider\$3	USPAT; EPO; JPO	OR	ON	2006/06/18 17:57
S10 3	53228	gate same divider\$3	USPAT; EPO; JPO	OR	ON	2006/06/18 17:57
S10 4	849	MOS same gate same divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:57
S10 5	437	MOS same gate with divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:58
S10 6	288	MOS with gate with divider	USPAT; EPO; JPO	OR	ON	2006/06/18 17:58
S10 7	218	MOS with gate with divider same voltage	USPAT; EPO; JPO	OR	ON	2006/06/18 17:58
S10 8	81	MOS with gate with divider same voltage same input	USPAT; EPO; JPO	OR	ON	2006/06/18 18:00
S10 9	0	MOS with gate with divider same voltage same input same surge	USPAT; EPO; JPO	OR	ON	2006/06/18 18:00
S11 0	0	MOS with gate with divider same voltage same input same (surge overvoltage transient)	USPAT; EPO; JPO	OR	ON	2006/06/18 18:01
S11 1	5	MOS with gate with divider same voltage same (surge overvoltage transient)	USPAT; EPO; JPO	OR	ON	2006/06/18 18:02
S11 2	1	MOS with gate near5 divider same voltage same (surge overvoltage transient)	USPAT; EPO; JPO	OR	ON	2006/06/18 18:04
S11 3	179	MOS with gate with divider near3 voltage	USPAT; EPO; JPO	OR	ON	2006/06/18 18:05

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S11 4	54	MOS with gate with divider near3 voltage and voltage near5 limit\$5	USPAT; EPO; JPO	OR	ON	2006/06/18 18:09
S11 5	18	MOS with gate with divider near3 voltage and voltage near5 limit\$5 and protection	USPAT; EPO; JPO	OR	ON	2006/06/18 18:14
S11 6	9	MOS with gate with divider near3 voltage same protection	USPAT; EPO; JPO	OR	ON	2006/06/18 19:11
S11 7	1	"3991381".pn.	USPAT; EPO; JPO	OR	ON	2006/06/18 19:15
S11 8	5122	(shunt shunting shunts clamp clamps clamping) same (mosfet fet nfet pfet)	USPAT; EPO; JPO	OR	ON	2006/06/18 19:20
S11 9	674	(shunt shunting shunts clamp clamps clamping) same ((mosfet fet nfet pfet) with resistor\$1 with gate)	USPAT; EPO; JPO	OR	ON	2006/06/18 19:21
S12 0	191	(shunt shunting shunts clamp clamps clamping) same (((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing))	USPAT; EPO; JPO	OR	ON	2006/06/18 19:25
S12 1	62	(shunt shunting shunts clamp clamps clamping) same (((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/18 19:28
S12 2	31	(shunt shunting shunts clamp clamps clamping) same (((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing)) and protection and divider	USPAT; EPO; JPO	OR	ON	2006/06/18 19:28
S12 3	42	(shunt shunting shunts clamp clamps clamping) same (((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing)) and protection and divid\$4	USPAT; EPO; JPO	OR	ON	2006/06/18 19:36
S12 4	19	(trigger\$5) same (((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing)) and protection and divid\$4	USPAT; EPO; JPO	OR	ON	2006/06/18 19:40

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S12 5	3	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (bias biased biasing)) and protection and divid\$4 same predetermin\$3 with ratio	USPAT; EPO; JPO	OR	ON	2006/06/18 19:43
S12 6	10	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (voltage)) and protection and divid\$4 same predetermin\$3 with ratio	USPAT; EPO; JPO	OR	ON	2006/06/19 10:21
S12 7	4	(((mosfet fet nfet pfet) with resistor\$1 with gate with capacitor) same (voltage)) and protection and divid\$4 same predetermin\$3 with ratio	USPAT; EPO; JPO	OR	ON	2006/06/19 10:23
S12 8	357	(((mosfet fet nfet pfet) with resistor\$1 with gate with capacitor) same (voltage)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:23
S12 9	1504	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (voltage)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:24
S13 0	83	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (over\$oltage)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:33
S13 1	1	(((mosfet fet nfet pfet) with resistor\$1 with gate) same trigger same (over\$oltage)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:34
S13 2	94	(((mosfet fet nfet pfet) with resistor\$1 with gate) same trigger) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:45
S13 3	269	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (clamp clamping clamps shunt shunts shunting)) and protection	USPAT; EPO; JPO	OR	ON	2006/06/19 10:46
S13 4	261	(((mosfet fet nfet pfet) with resistor\$1 with gate) same (clamp clamping clamps shunt shunts shunting)) and (overvoltage surge transient)	USPAT; EPO; JPO	OR	ON	2006/06/19 10:47

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S13 5	122	(((mosfet fet nfet pfet) with resistor\$1 with gate) with (clamp clamping clamps shunt shunts shunting)) and (overvoltage surge transient)	USPAT; EPO; JPO	OR	ON	2006/06/19 10:48
S13 6	19	(((mosfet fet nfet pfet) with resistor\$1 with gate) with (clamp clamping clamps shunt shunts shunting)) same (overvoltage surge transient)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:00
S13 7	25	(((mosfet fet nfet pfet) same resistor\$1 with gate) with (clamp clamping clamps shunt shunts shunting)) same (overvoltage surge transient)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:10
S13 8	41	(((mosfet fet nfet pfet) same resistor\$1 same gate) with (clamp clamping clamps shunt shunts shunting)) same (overvoltage surge transient esd)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:18
S13 9	2	(((mosfet fet nfet pfet) same divid\$3 same gate) with (clamp clamping clamps shunt shunts shunting)) same (overvoltage surge transient esd)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:21
S14 0	10	(((mosfet fet nfet pfet) same divid\$3 same gate) with (clamp clamping clamps shunt shunts shunting)) and (overvoltage surge transient esd)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:23
S14 1	12	(((mosfet fet nfet pfet) same divid\$3 same gate) with (voltage with limit\$3)) and (overvoltage surge transient esd)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:49
S14 2	35	(((mosfet fet nfet pfet) same resistor same zener same gate) with (voltage with limit\$3)) and (overvoltage surge transient esd)	USPAT; EPO; JPO	OR	ON	2006/06/19 11:50